**KAPILESWARREDDY S**

**PROFILE**

Post graduating student eager to contribute to team success through smart work Attention to detail and excellent organizational skills. Clear knowledge on standard cell design and characterization and trained in VLSI domain. Motivated to learn, grow and excel in VLSI and semiconductor industry

**EDUCATION**

* **M.Tech** **VLSI Design**

**CGPA – 7.88 / 10**

Amrita Vishwa Vidyapeetham

* **B.Tech** **Electronics and Communication Engineering**

**CGPA – 8.85 / 10**

Sri Chendrashekarendra Saraswathi Vishwa Maha Vidyalaya (SCSVMV)

* **Class 12** – 85.8%
* **Class 10** – 85%

**TECHNICAL INTERESTS**

VLSI Design, Microcontrollers

**PROJECTS**

**Design and Implementation of SDRAM and SDRAM controller:**

the design of SDRAM controller and SDRAM is carried out in Vivado 2019.1 aiming to check the functionality by implementing it in Zed board. The output analysis is represented using virtual input output IP and the triggered ports are observed using the Integrated logic analyzer.

**Vehicle speed control using embedded system:**

The research focus on unifying the Global Positioning system with embedded wireless system is the new approaches in intelligent vehicle control for critical remote location application using ARM. In conventional system they are designed to control the speed of vehicles in all days. The main objective of the proposed system is to operate the vehicle in safe speed at critical zones.

3.Smart GAS cylinder monitoring and booking system: the research is focus on load cell technology to check weight of the cylinder and MQ5 GAS sensor to detect the gas leakage, Solenoid valve to turn off the regulators if any gas leakage is occurred and GSM module used for sending message to the user and if cylinder is empty. it will automatically book the new one.

**Washing machine using FSM (Finite State Machine):**

using Verilog HDL and mealy technique in vivado 2019.1 tool we achieved it.

**Flood monitoring system**:

The research focus on using proteus tool to design the circuit, embedded c programming to run the system circuit which is built mainly on the Arduino and proximity sensors.

**TECHNICAL SKILLS**

STM32 Basic

ARDUINO UNO

Verilog

System Verilog

EMBEDDED C / OPENCV

RTL design

FPGA Implementation

ASIC Implementation

Cadence design flow

Design verification

STA

NOC

PYTHON3 / Anakonda

XILINX / MODELSIM

Proteus

Model Sim / Questa sim

DSCH

Microwind

**INTERNSHIP**

**BHARAT SANCHAR NIGAM LIMITED (BSNL)- TELECOM TECHNOLOGIES JUNE 2019** brief discussion about the telecom technologies which are operating under the BSNL

**LANGUAGES**

English, Telugu, Hindi